

## Refine Search

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SCHEDULABE	1
SCHEDULABLE	519
SCHEDULABLES	1
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SCHEDULAR	158
SCHEDULAS	1
(SCHEDUL\$5 NEAR12 (BREAK\$6 OR STOP\$4 OR INHIBIT\$4 OR HALT\$4 OR SUSPENSION\$1 OR SUSPEND\$3) NEAR45 PIPELIN\$7).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	44

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### Search History

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<u>L42</u>	l41 and l23 (schedul\$5 or arbitrar\$3 or random\$3 or uncondition\$3 or user or desir\$6 or defin\$4) near12 (lock\$3 or interlock\$3 or suspension\$1 or suspend\$6 or inhibit\$4 or stop\$4 or halt\$3 or break\$7) near17 pipelin\$6	4 <u>L42</u> 334 <u>L41</u>
<u>L40</u>	l30 near55 (test\$4 or debug\$5 or scan\$5)	11 <u>L40</u>
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<u>L34</u>	L30 and l21	8 <u>L34</u>
<u>L33</u>	L30 and l6	0 <u>L33</u>
<u>L32</u>	L30 and l5	45 <u>L32</u>
<u>L31</u>	L30 and l4 (schedul\$5 or arbitrar\$3 or random\$3 or uncondition\$3 or user or desir\$6 or defin\$4) near12 (lock\$3 or interlock\$3 or suspension\$1 or suspend\$6 or inhibit\$4 or stop\$4 or halt\$3) near12 pipelin\$6	57 <u>L31</u> 266 <u>L30</u>
	<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>	
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<u>L28</u>	l20 and l5	22 <u>L28</u>
<u>L27</u>	l20 and l4	32 <u>L27</u>
<u>L26</u>	l20 and l23	3 <u>L26</u>
<u>L25</u>	l20 and l22	7 <u>L25</u>
<u>L24</u>	l20 and l21	7 <u>L24</u>
<u>L23</u>	(717/124-127, 140-167)![CCLS]	1759 <u>L23</u>
<u>L22</u>	(714/10-57)[CCLS]	13657 <u>L22</u>
<u>L21</u>	(712/242-248)![CCLS]	1467 <u>L21</u>
	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>	
<u>L20</u>	(arbitrar\$3 or random\$3 or uncondition\$3 or user or desir\$6 or defin\$4) near12 (lock\$3 or interlock\$3 or suspension\$1 or suspend\$6 or inhibit\$4 or stop\$4 or halt\$3) near12 pipelin\$6	211 <u>L20</u>
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<u>L16</u>	L15 and l4	13 <u>L16</u>
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<u>L13</u>	L10 and l6	1	<u>L13</u>
<u>L12</u>	L10 and l5	11	<u>L12</u>
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<u>L10</u>	(forc\$4 or uncondition\$3 or overrid\$3) near12 (suspension\$1 or suspend\$6 or inhibit\$4 or stop\$4 or halt\$3) near12 pipelin\$6	178	<u>L10</u>
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<u>L8</u>	l3 and 15	9	<u>L8</u>
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<u>L6</u>	(713/401,500-503,600-601 )![CCLS]	3762	<u>L6</u>
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<u>L4</u>	(712/2-300)[CCLS]	12791	<u>L4</u>
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<u>L3</u>	(forc\$4 or uncondition\$3 or overrid\$3) near12 (inhibit\$4 or stop\$4 or halt\$3) near12 pipelin\$6	127	<u>L3</u>
<u>L2</u>	uncondition\$3 near12 (inhibit\$4 or stop\$4 or halt\$3) near12 pipelin\$6	0	<u>L2</u>
<u>L1</u>	uncondition\$3 near5 (inhibit\$4 or stop\$4 or halt\$3) near8 pipelin\$6	0	<u>L1</u>

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Display Format:  Citation  Citation & Abstract

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STO IEEE Standard

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#### 1. Detection of obstacles in the flight path of an aircraft

Gandhi, T.; Mau-Tsuen Yang; Kasturi, R.; Camps, O.; Coraor, L.; McCandless, J.;  
*Aerospace and Electronic Systems, IEEE Transactions on*  
Volume 39, Issue 1, Jan. 2003 Page(s):176 - 191  
Digital Object Identifier 10.1109/TAES.2003.1188902

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#### 2. A Hardware IP-Core for Information Retrieval

Freeman, M.; Jayasooriya, T.;  
*Digital System Design Architectures, Methods and Tools, 2006, DSD 2006, 9th EUROMICRO Conf.*  
30-01 Aug. 2006 Page(s):115 - 122  
Digital Object Identifier 10.1109/DSD.2006.8

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#### 3. Stage-skip pipeline: a low power processor architecture using a decoded instruction buffer

Hiraki, M.; Bajwa, R.S.; Kojima, H.; Gorni, D.J.; Nitta, K.; Shri, A.;  
*Low Power Electronics and Design, 1996, International Symposium on*  
12-14 Aug. 1996 Page(s):353 - 358  
Digital Object Identifier 10.1109/LPE.1996.547538

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